

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1428068	(variable adj2 filter) or filter	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:10
L2	7797	(discard or reject) adj2 (calls or traffic or data or measurements)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:11
L3	2299	1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:11
L4	2154682	(processor or microprocessor or controller or microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:11
L5	1936	3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:12
L6	309232	(mobile or wireless or cellular) adj2 (device or phone or terminal or telephone)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:12
L7	343	5 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:13
L8	33383	(detect or obtain or measure) adj2 (load or traffic or calls)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:13
L9	33	7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:18
L10	491	statistics adj2 (controller or processor or microprocessor or microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:19
L11	205710	(detect or obtain or measure) adj2 (load or traffic or calls or signals)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:19
L12	36	10 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:31

L13	57225	(stat\$6) adj2 (controller or processor or microprocessor or microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:34
L14	7152	variable adj2 filter	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:32
L15	80	13 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:32
L16	491	(statistics) adj2 (controller or processor or microprocessor or microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:16
L17	6	14 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:34
L18	445	(variable adj2 filter) with (load or traffic or data or calls)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:36
L19	1116493	(processor or microprocessor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:37
L20	170	18 and 19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:40
L21	79117	(processor or microprocessor) adj2 (load or traffic or data or measurements)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:40
L22	590	(processor or microprocessor) adj2 (load or traffic or data or measurements) with (filter or (variable adj2 filter))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:41
L23	236	(control or vary or adjust) adj2 (processor adj2 load)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:42
L24	6	22 and 23	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:43

L25	4124	(radio adj2 network adj2 controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:44
L26	144304	(base adj2 stations) or (BTS)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:44
L27	46525	(radio adj2 network adj2 controller) or (base adj2 station adj2 controller) or (BSC) or (BTS)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:45
L28	77675	(operations or maintainance) adj2 (station or center or controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:46
L29	2746	27 and 28	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:46
L30	2633	4 and 29	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:47
L31	91164	"455"/\$.ccis.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:47
L32	1421	30 and 31	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:47
L33	14065	(processor or microprocessor) adj2 (load or traffic or calls)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 11:47
L34	57	32 and 33	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 12:41
L35	2752197	enable or (start adj2 up) or (turn adj2 on)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 12:54
L36	413	(load) adj2 (above adj2 (threshold or limit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:13

L37	660	load adj2 (below adj2 (threshold or limit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 12:55
L38	2154682	(processor or microprocessor or controller or microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 12:56
L39	83	36 and 37 and 38	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:12
L40	825	statistics adj2 (processor or controller or counter or microprocessor or microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:20
L41	10499	(load or traffic or calls) adj2 (threshold or limit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:14
L42	14	40 and 41	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:16
L43	25190	(controller or processor or microprocessor or microcontroller) adj2 (load or traffic or calls)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:16
L44	107	40 and 43	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:17
L45	91164	"455"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:17
L46	1	44 and 45	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:17
L47	91164	"455"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:18
L48	12	40 and 47	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/10/28 13:18